

# Liberum Future of E-Mobility Conference Semiconductors enabling Automated Driving

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## Agenda

- 1 Automated Driving
- 2 Sensor technologies
- 3 Computing Requirements
- 4 Dependability Requirements

## Long-term semi content drivers intact; improved market position in all addressed product categories



#### Strong drivers for semi content per car

#### electro-mobility



#### automated driving



comfort, premium



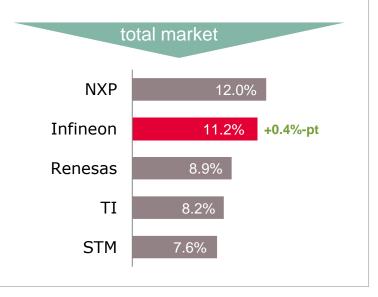
driven by legislation

2019-12-11

- all kinds of xEV, including 48 V
- today China; tomorrow Europe
- near-term L1/L2/L2+
- long-term L3/L4/L5
- need for dependable functionalities\* (e.g. sensors, power supplies, computing power)
- comfort features trickling down from high- to mid-range
- user experience
- lighting
- replacement of hydraulic and electromechanical units

## **Automotive semiconductor market** 2018 total market size: \$37.7bn

- #1 in power: market share of 26.2% (+0.2 %-pt)
- #2 in sensors: market share of 13.4% (+0.5 %-pt)
- #4 in μC: market share of 9.1% (+0.6 %-pt)



Source: Strategy Analytics, "Automotive Semiconductor Vendor Market Shares", April 2019

<sup>\*</sup> For more information on "dependable functionalities" please see slide 12.

# L2+ becomes the new L3 as legislation is delayed; but features will be implemented



#### Legal regulation

- > it requires that the vehicle is under continuous control of the driver (Art. 8 UN regulation)
- > it doesn't allow the approval of "Autonomous Steering Systems" (ECE) in normal operation without human activation (e.g. flashing indicator)

### L2+ = L3 function + L2 driver obligation (e.g.





 Driver need to intervene and rapidly take over control of the vehicle when automation fails



 Driver assistance system is capable of controlling speed and direction but requires an attentive driver



Daimler's "Drive Pilot" solves the issue of unclear / undefined L3 regulation:

- the fallback-ready user must resume manual driving at any time
- the "driver readiness" is continuously monitored
- the driver is not permitted to sleep or to leave the driver seat
- autom. lane changes are not allowed

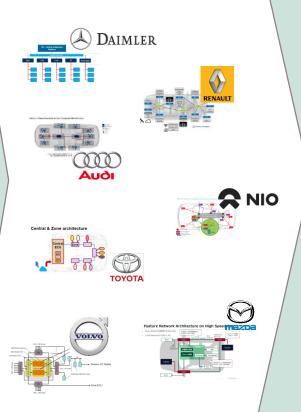
https://www.daimler.com/innovation/case/autonomous/drive-pilot-2.html

## The architecture must change to support xEV, AD, SOTA and enabling MaaS while balancing system cost



- ADAS and AD requires
   hierarchical architecture
- Manage complexity introduced by new/advanced functionalities
- Address increased safety and security requirements
- Increase flexibility and enable "upgradability" (SOTA)
- Optimize system cost at low power consumption

**Different OEMs approaches** (evolutionary or disruptive)



These approaches can be clustered with good approximation into the following **categories**:

Domain Control
Domain Integration
Zone Architecture
Car Computer

AD = automated driving; SOTA = software over the air; MaaS = mobility as a service

## OEM road-maps in E/E architectures; architecture will converage towards zone-based architecture



	today	start of production: 2023/24	start of production: 2025/26	start of production: 2028/29
Volume OEMs (up to L2)	Gateway  CAN1  Chassis powertrain body	Domain integration  TOU  Infotament  AM CANED CANE CANE CANE CANE CANE CANE CANE CANE	Hybrid zone-low  Car computer TCU  En En VCU  Conceding layer  Actuation layer  Body-zones	Hybrid zone-mid + car computer  Car computer
Premium OEMs (up to L2+)	Domain controllers  TGU  TGU  TGU  TGU  TGU  TGU  TGU  TG	Various directions - System in transition - Higher domain integration - Zone-low & mid	Hybrid zone-mid  Car computer Infotainment  VCU Eth CAN Eth CA	Car computer  Car computer  Car computer  TCU  Screens  Computing layer  C

Note: most probable scenario seen by Infineon

## Increased sensor requirements drive the content in the next five years and beyond



#### More sensors required for any next level of automation

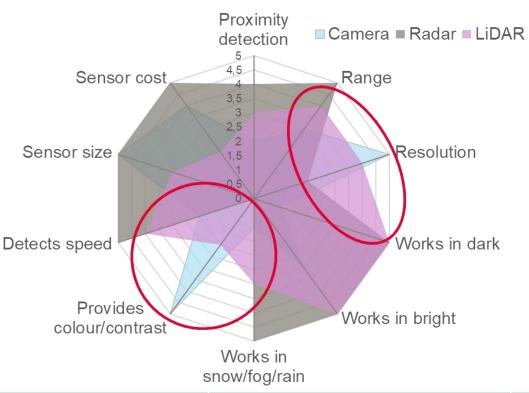
	NCAP 5 Star, AD L2	AD L2+/L3	AD L4/L5		
	Automatic emergency brake/ forward collision warning				
Application*	Parking assist		Valet parking		
	Lane keep assist	Highway assist	Highway and urban chauffeur		
Radar # of modules**	Corner MRR/LRR  ≥ 3  New: Corner; starting 2020	MRR/LRR Corner	≥ 6 Imaging ≥ 10 Surround		
Camera # of modules**	≥ 1		≥ 4 ≥ 8		
Lidar # of modules**	0		≤1 ≥1		
Others	) Ultrasonic	<ul><li>) Ultrasonic</li><li>) Interior camera</li></ul>	<ul><li>&gt; Ultrasonic</li><li>&gt; Interior camera</li><li>&gt; V2X</li></ul>		

<sup>\*</sup> Source: VDA (German Association of the Automotive Industry); Society of Automotive Engineers

<sup>\*\*</sup> market assumption

## We need 3 sensor technologies to have always data from two sensors available $\rightarrow$ 2 out of 3

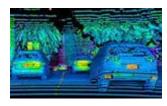




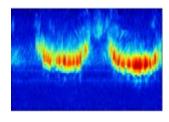
#### Camera



Lidar



Radar

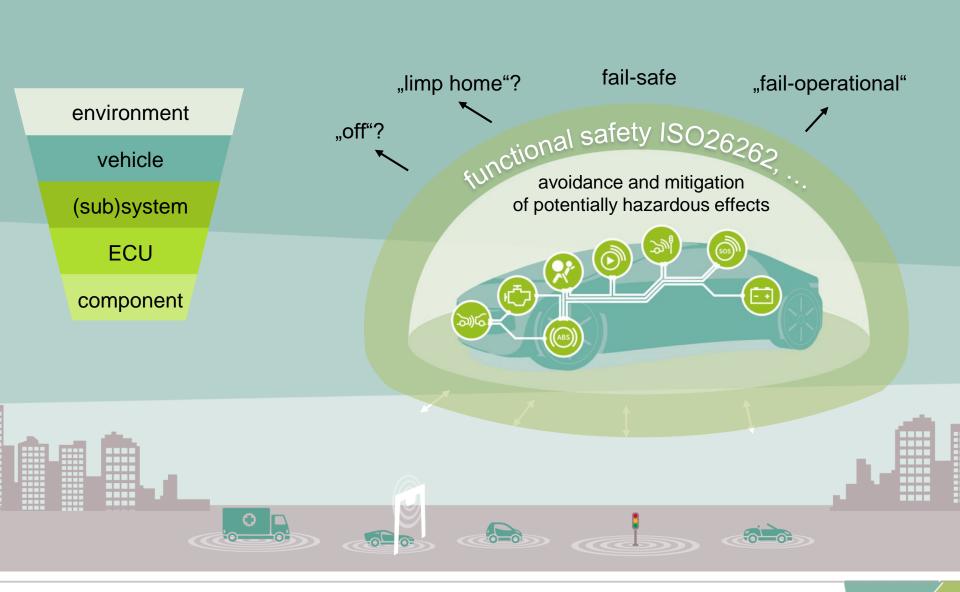


criteria	Good weather	Bad weather
range	Radar / Lidar	Radar / Lidar
resolution	Camera / Lidar	Lidar / Radar
speed detection	Radar / Lidar	Radar / Lidar
contrast	Camera / Lidar	Lidar / Camera

Note: first technology shows best performance

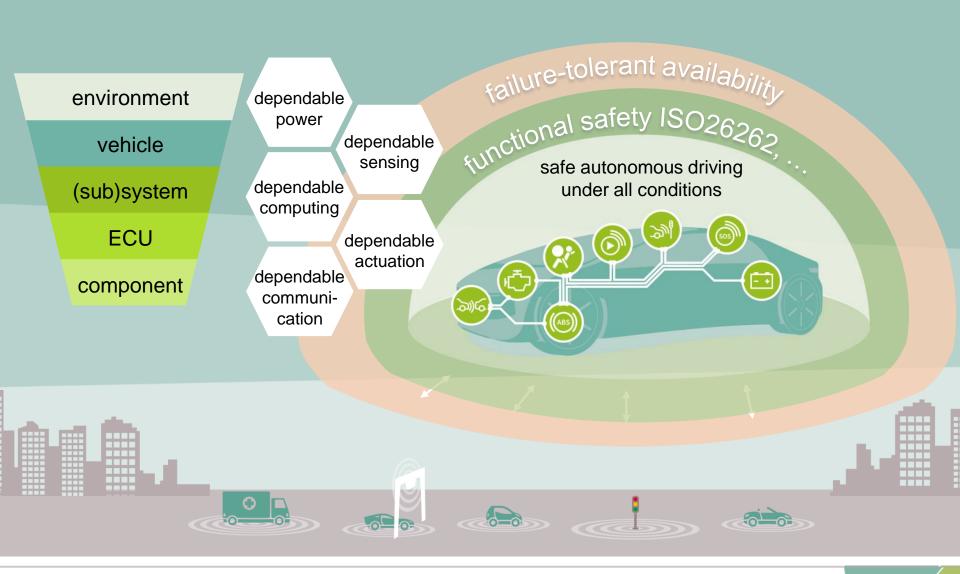
## Vision Zero – AD requires failure-tolerant availability of the system, "better than a human"





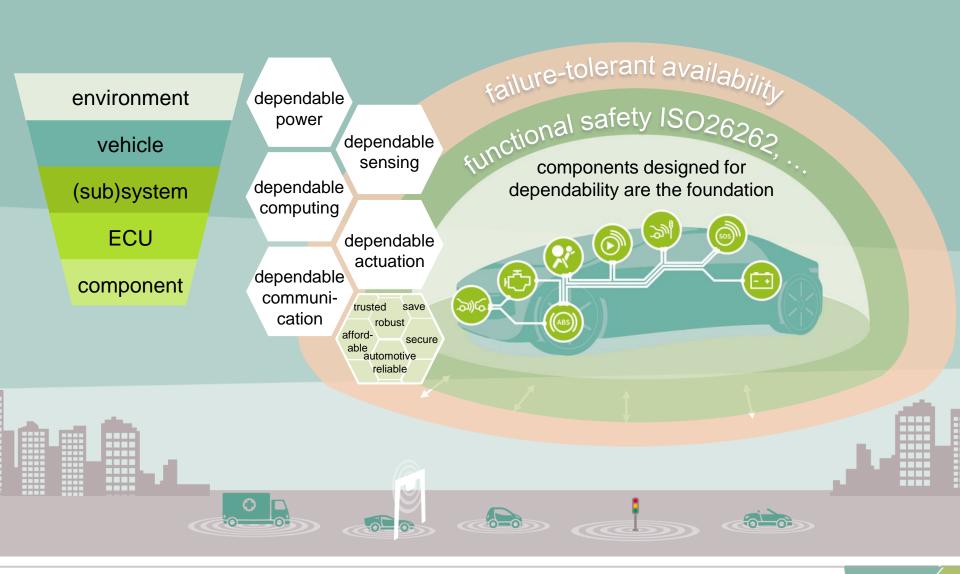
## Vision Zero – AD requires failure-tolerant availability of the system, "better than a human"





# Vision Zero – taking the next step from functional safety to failure-tolerant availability

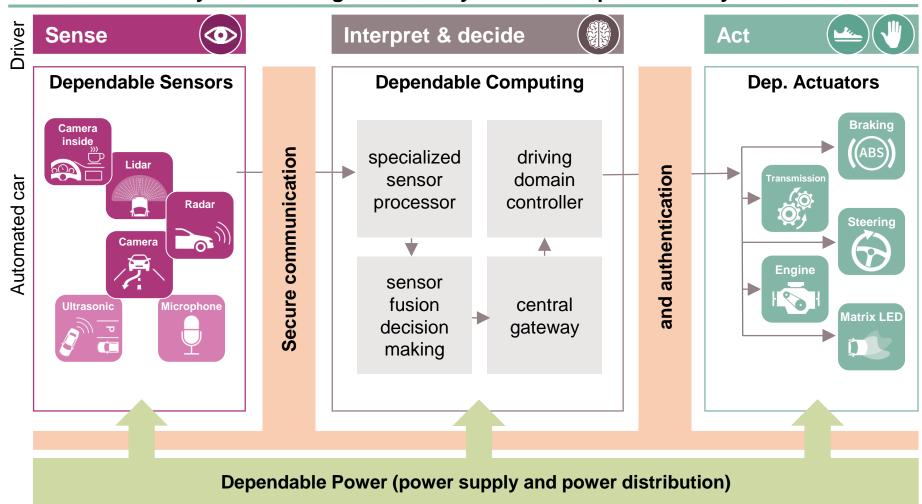




## Vision Zero – AD requires failure-tolerant availability of the system in the environment, "better than a human"



#### A failure-tolerant system with high availability relies on dependable key functionalities



## AURIX<sup>™</sup> first-choice microcontroller dependable systems enabling ADAS/AD platforms

Optional





- AURIX<sup>™</sup> family provides leading technology for sensor fusion either as main fusion computer for L1/L2 or as host controller for higher autonomy levels.
- Major OEMs from Europe, Japan, Korea, China, and North America will ramp production in 2021.

# Level 2 ≥ Level 2+ | Radar | Infine on | AURIX™ | AURIX™ | AURIX™ | AURIX™ | GPU/CPU | GPU/CP

#### **AURIX™** functionalities

- fusion and decision-making
- safety management
- security management
- vehicle gateway

#### **AURIX™** functionalities

- parallel usage to enable scalability with compatibility
- safety management
- security management

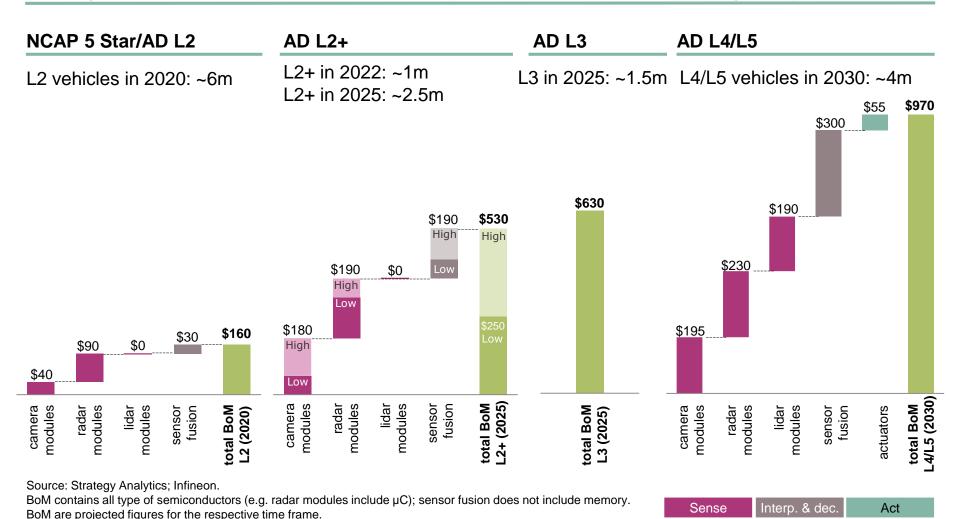
#### **AURIX™** functionalities

- host controller for the data fusion processor
- > enables ISO 26262 ASIL-D
- emergency response in case of a GPU/CPU fail

## ADAS/AD semi growth driven by radar and camera sensor modules over the next 5 years



#### Average semiconductor content per car by level of automation at the given years



## Semiconductors enable the future of driving: More safety, more comfort, less pollution







Part of your life. Part of tomorrow.